DIGITAL LINE TEST EQUIPMENT

Patent Number:

JP62185424

Publication date:

1987-08-13

Inventor(s):

TAKEMASA ATSUSHI

Applicant(s)::

MATSUSHITA ELECTRIC IND CO LTD

Requested Patent:

JP62185424

Application Number: JP19860025806 19860210

Priority Number(s):

IPC Classification:

H04J3/12; H04B17/00

EC Classification:

Equivalents:

Abstract

PURPOSE: To facilitate capacity test on a line with large capacity by providing a test equipment of a digital line comprising a full duplex transmission circuit transmitting/receiving a random data for line test to/from a time multiplex communication line, an instant comparison circuit of the data, an error measurement circuit, a microcomputer circuit and an intercomputer coupling circuit and conducting the automatic line test by a remote controller.

CONSTITUTION:A line test start is commanded from the remote controller (a). The microcomputer circuit 1 issues a test start command to a data storage section 2 by the line test start command and the data storage section 2 outputs a test data to a time division multiplex communication line (e) via an output control section 4. In detecting a transmission time slot in such a case, a time slot detection section 6 supplies it to the output control section 4 and sends the test data to the time division multiplex communication line (e). The data is sent to a designated transmission line, reflected by a line terminator (b) and the reception side fetches the test data to an input control section 5 every time the time slot detection section 6 detects the received time slot. Then, the corresponding test data of the data storage section 2 is referenced and compared and the error number is counted and stored in a data comparison section 3. The microcomputer circuit 1 sends the result to the remote controller (a) via the intercomputer coupling circuit (f).

Data supplied from the esp@cenet database - I2